

	Type	L #	Hits	Search Text	DBs	Time Stamp
5	BRS	L6	25	((storage memory register) and counter and (driver buffer transmitter receiver transceiver) and strength).clm.	US-PGPUB	2005/10/07 14:17
6	BRS	L7	8	((storage memory register) and (strength adj (value count)) and (driver buffer transmitter receiver transceiver) and adjust\$4).clm.	US-PGPUB	2005/10/07 14:18